

## **EMP Note:** Create a C project in CoIDE.

Select: <Project><New Project>:



Chose a Project Name, a Project Path and push <Next>:



Select: "Model based on chip" and push <Next>:

E - C LM3S3000	Manufacturer: Series: Device: Toolset:	TI LM3S6000 LM3S6965 ARM
M356000     M356000     MI356432     M356432     M356537     MI356510     M356911     M356918     M356918     M356958     M356958     M356958	LM3S6965 Descriptions: "T1's LM3S6965 o 50MHz and featu 64 KB single-cycle Key Parameters: 50MHz, 256 k cycle SRAM,	perates at a maximum speed of res 256 KB single-cyde flash and e SRAM." B single-cyde flash, 64 KB single- MPU

Select: "TI/LM3S6000/LM3S6965" and push <Finish>.



Check the "BOOTLOADER startupcode" in the Repository panel.



Now, select: "View/Configuration/compile" and add an Includepath to your Im3s6965.h file.

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	Configuration
	Device Compile Link Output User Debugger Download
	Contraile Options FPU: Not use FPU V Optionization: None (-00)
	//ib/stellarisware/inc Remove
ļ	Misc Controls  Compiler Control String  - Compiler Control String  - mcpu=cortex-m3; -mthumb; -Wali; -ffunction-sections; -g; -O0; -DLM356965; -1.; -1//.Nb
	L



Finally configure your target hardware by selecting: "View/Configuration/Debugger". Choose Adapter = "Stelaris-ICDI".

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	🚳 welcome	Repository	Configuration 🕱	
	Configurat	ion		
	Device Compi	le Link Output	User Debugger Download	
	Hardware -			
	Adapter S	tellaris-ICDI	<b>•</b>	Port JTAG
	Startup Run to m	ain		
	Advance		Semibosting Enable	CachePo
	Reset Mode		Seminosung Enable	
	TargetInfo - Host IP Addr	ess: 127 . 0	. 0 . 1	

Now you are ready to code.

Good luck, Morten